

Claims:

1. Apparatus comprising:
 - a receiver (23) for receiving an audio file signal;
 - 5 a decoder (22) for demodulating said audio file signal; and
 - a processor (24) for polling said decoder for a loss of a phase lock in said demodulating of said audio file signal.
2. The apparatus of claim 1, wherein said processor resets and reinitializes said decoder
10 in response to said loss in said phase lock.
3. The apparatus of claim 1, wherein said receiver comprises 900 MHz radio frequency reception circuitry.
- 15 4. The apparatus of claim 1, wherein said decoder comprises an eight-to-four modulation EFM decoder.
5. The apparatus of claim 1, wherein said decoder outputs a digital audio stream.
- 20 6. The apparatus of claim 5, wherein said digital audio stream conforms to an I2S audio stream.
7. A computer readable medium containing software instructions that, when executed by a processor, perform the steps of:
 - 25 receiving a modulated audio file signal;
 - demodulating said modulated audio file signal;
 - polling said demodulating for a loss in a phase lock in said demodulating; and
 - resetting and reinitializing said demodulating in reply to said loss in said phase lock.
- 30 8. The computer readable medium according to claim 7, wherein said demodulating is a digital eight-to-fourteen modulation digital decoding.

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9. The computer readable medium according to claim 7, wherein said receiving is synchronized to a 900 MHz range carrier frequency modulated by said audio file signal.

10. The computer readable medium according to claim 7, wherein said demodulating
5 outputs a digital audio stream.

11. The computer readable medium according to claim 7, wherein said polling is carried out by a processor.

10 12. A method for detecting a signal loss in a wireless audio file signal transmission, said method comprising the steps of:

receiving an audio file signal;
decoding said audio file signal; and
polling (32) said decoding for a loss of a phase lock in said decoding of said audio

15 file signal..

13. The method of claim 12, further comprising the step of resetting and reinitializing (31) said decoding in response to said loss in said phase lock in said decoding.

20 14. The method of claim 12, wherein said step of receiving comprises 900 MHz range carrier frequency synchronizing.

15. The method of claim 12, wherein said step of decoding comprises an eight-to-fourteen bit modulation EFM decoding.

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16. The method of claim 12, wherein said step of decoding outputs a digital audio stream.

17. The method of claim 16, wherein said digital audio stream conforms to an I2S audio stream.